

OCT 13 2006

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FROM: Thomas Chan

DATE:

October 13, 2006

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**OFFICIAL FILING**

Attorney Docket No.: 333772000800  
 Group Art Unit: 2128  
 Examiner: S. Lo  
 Serial No.: 10/772,434  
 Filing Date: February 6, 2004  
 Inventors: Ramachandran KRISHNASWAMY et al.  
 Title: METHOD AND STRUCTURE TO DEVELOP A TEST  
 PROGRAM FOR SEMICONDUCTOR INTEGRATED  
 CIRCUITS

Papers enclosed herewith:

1. Transmittal Form - 1 page
2. Amendment in Response To Non-Final Office Action - 12 pages

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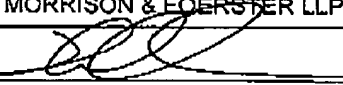
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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)		Application Number	10/772,434
		Filing Date	February 6, 2004
		First Named Inventor	Ramachandran KRISHNASWAMY
		Art Unit	2128
		Examiner Name	S. Lo
Total Number of Pages in This Submission	13	Attorney Docket Number	333772000800

ENCLOSURES (Check all that apply)		
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Docket No.: 333772000800  
(PATENT)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Ramachandran KRISHNASWAMY et al.

Application No.: 10/772,434

Confirmation No.: 5995

Filed: February 6, 2004

Art Unit: 2128

For: METHOD AND STRUCTURE TO DEVELOP  
A TEST PROGRAM FOR SEMICONDUCTOR  
INTEGRATED CIRCUITS

Examiner: S. Lo

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

This is in response to the Non-Final Office Action dated July 17, 2006 (Paper No./Mail Date 20060710), for which a response is due on October 17, 2006. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.